

FIG. 1

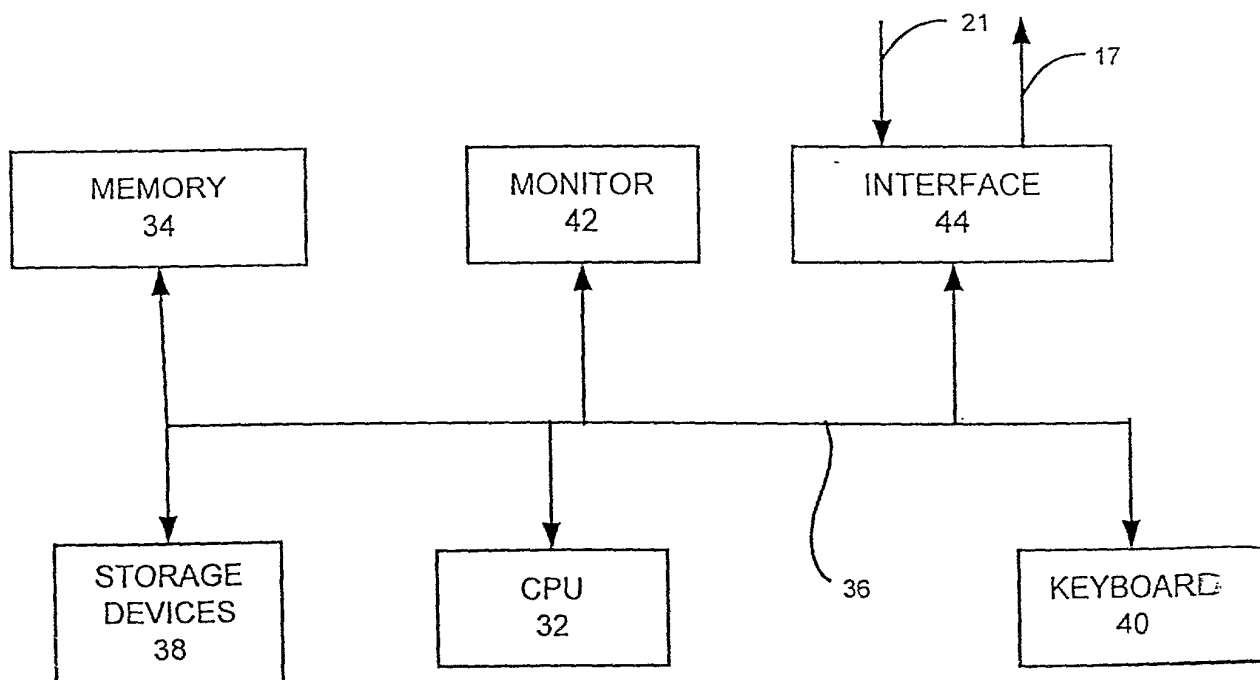


FIG. 2

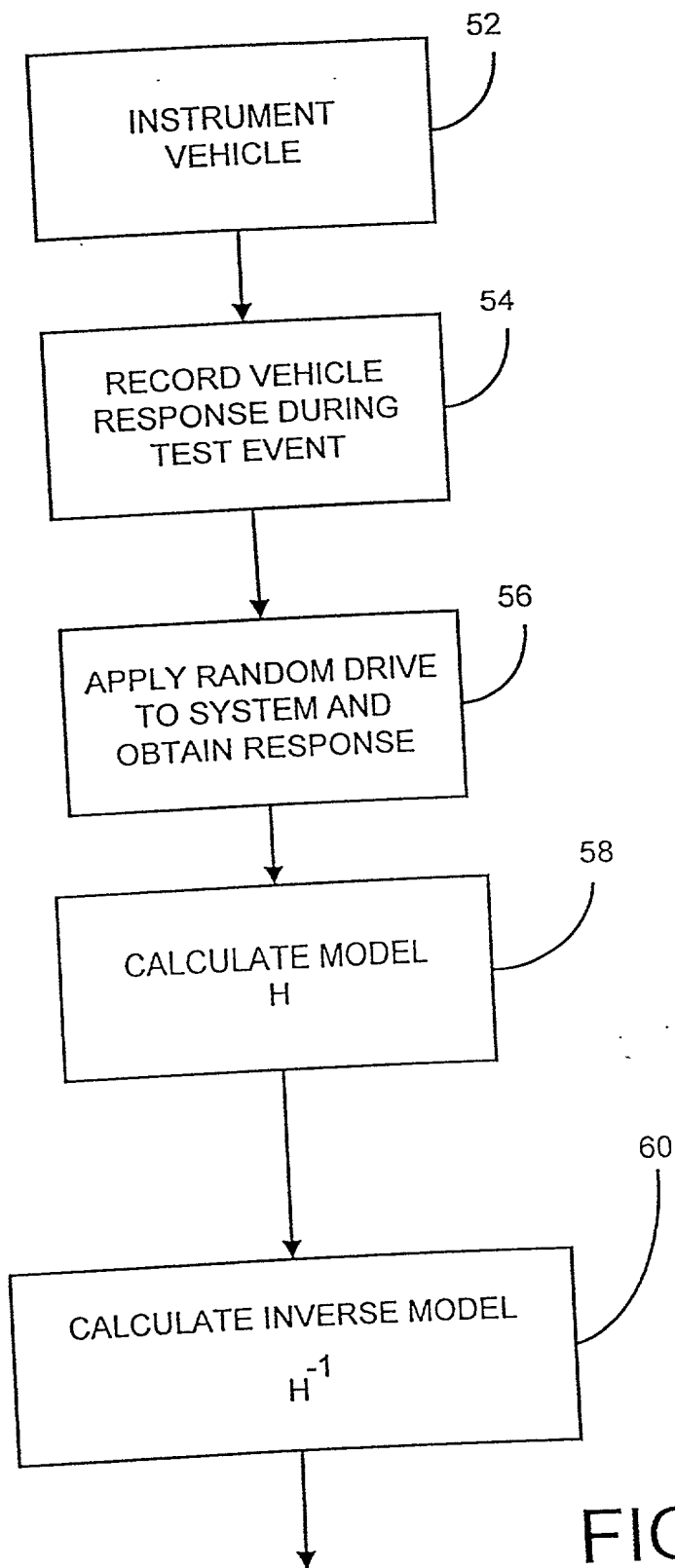


FIG. 3A

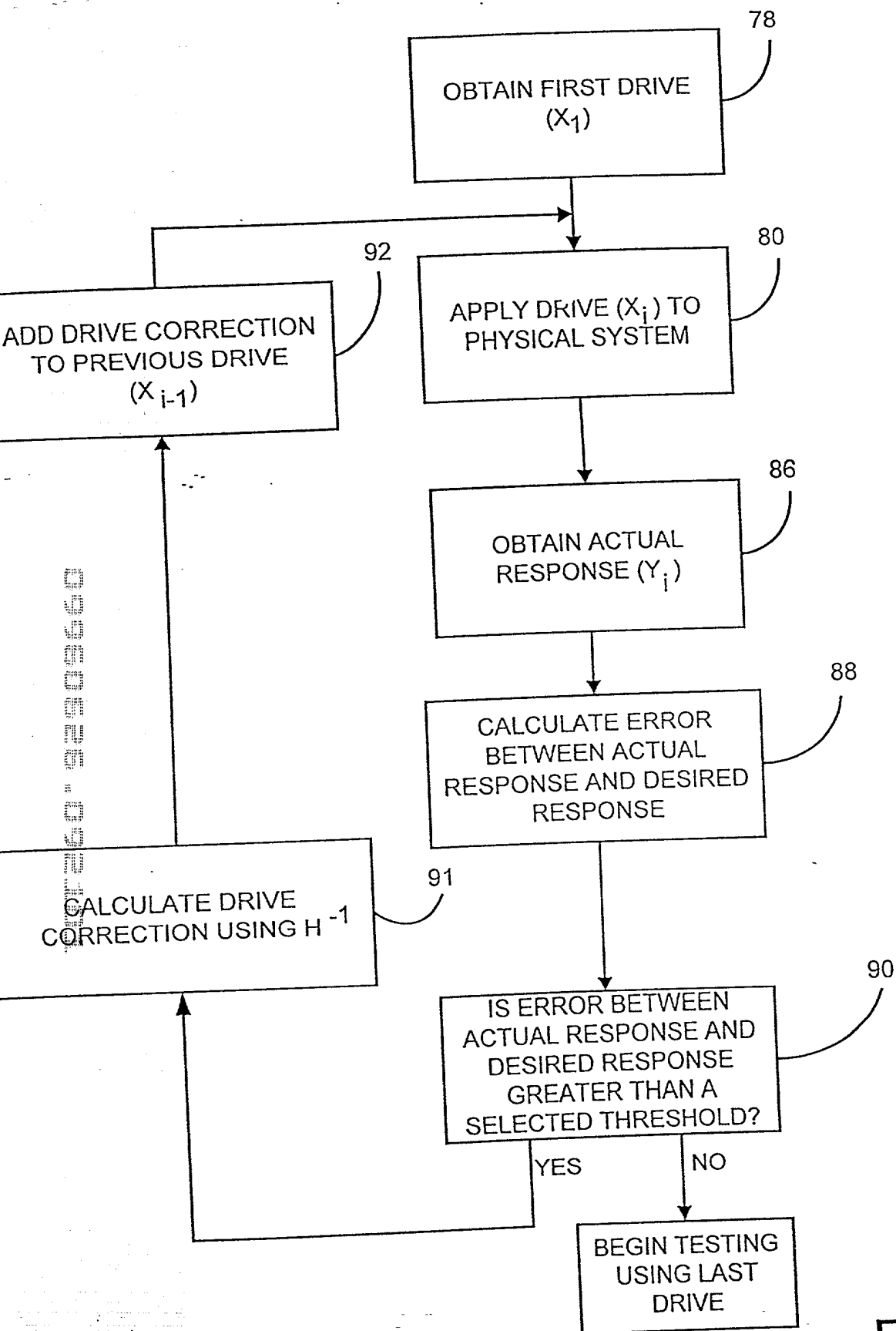


FIG. 3B

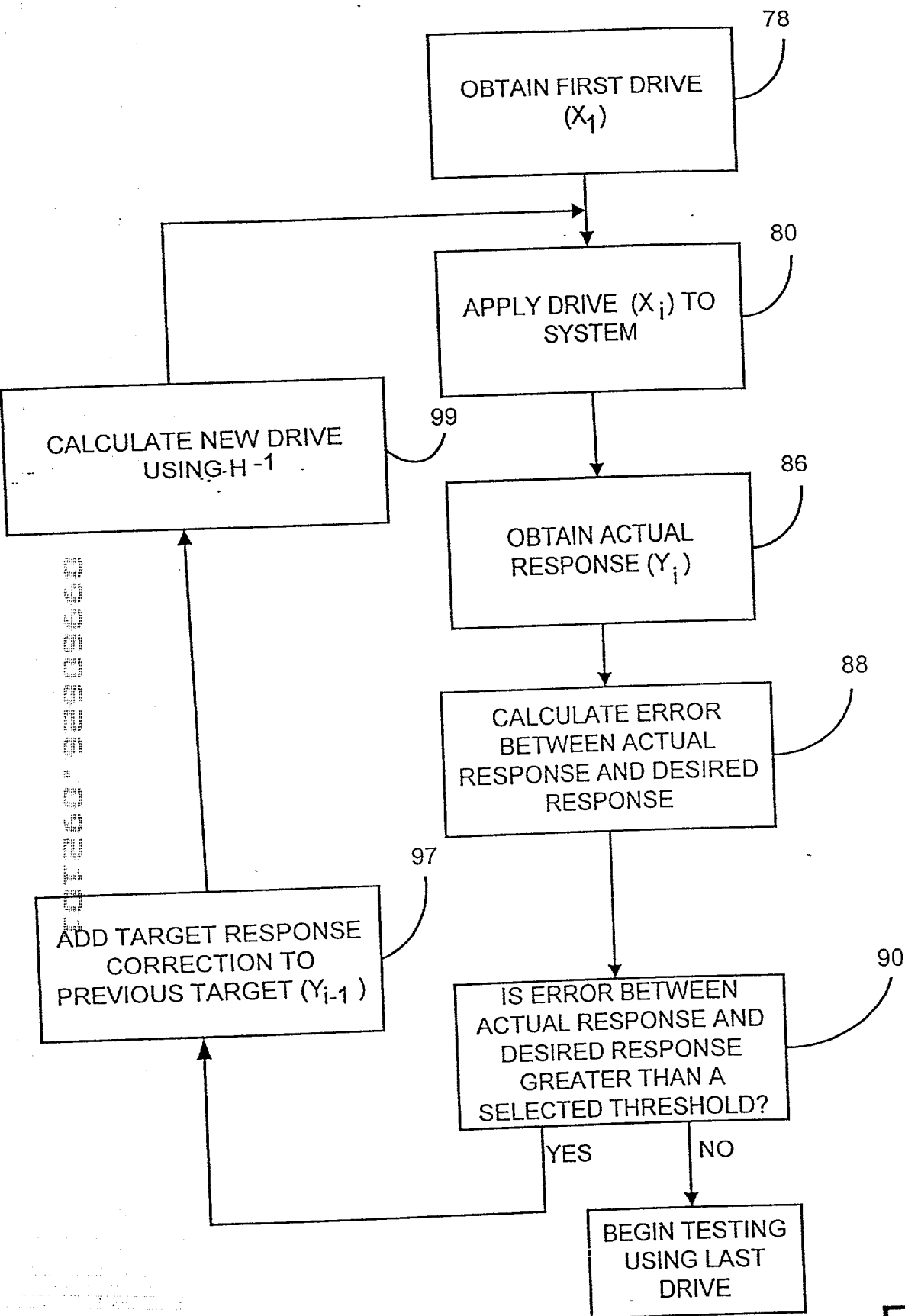


FIG. 3C

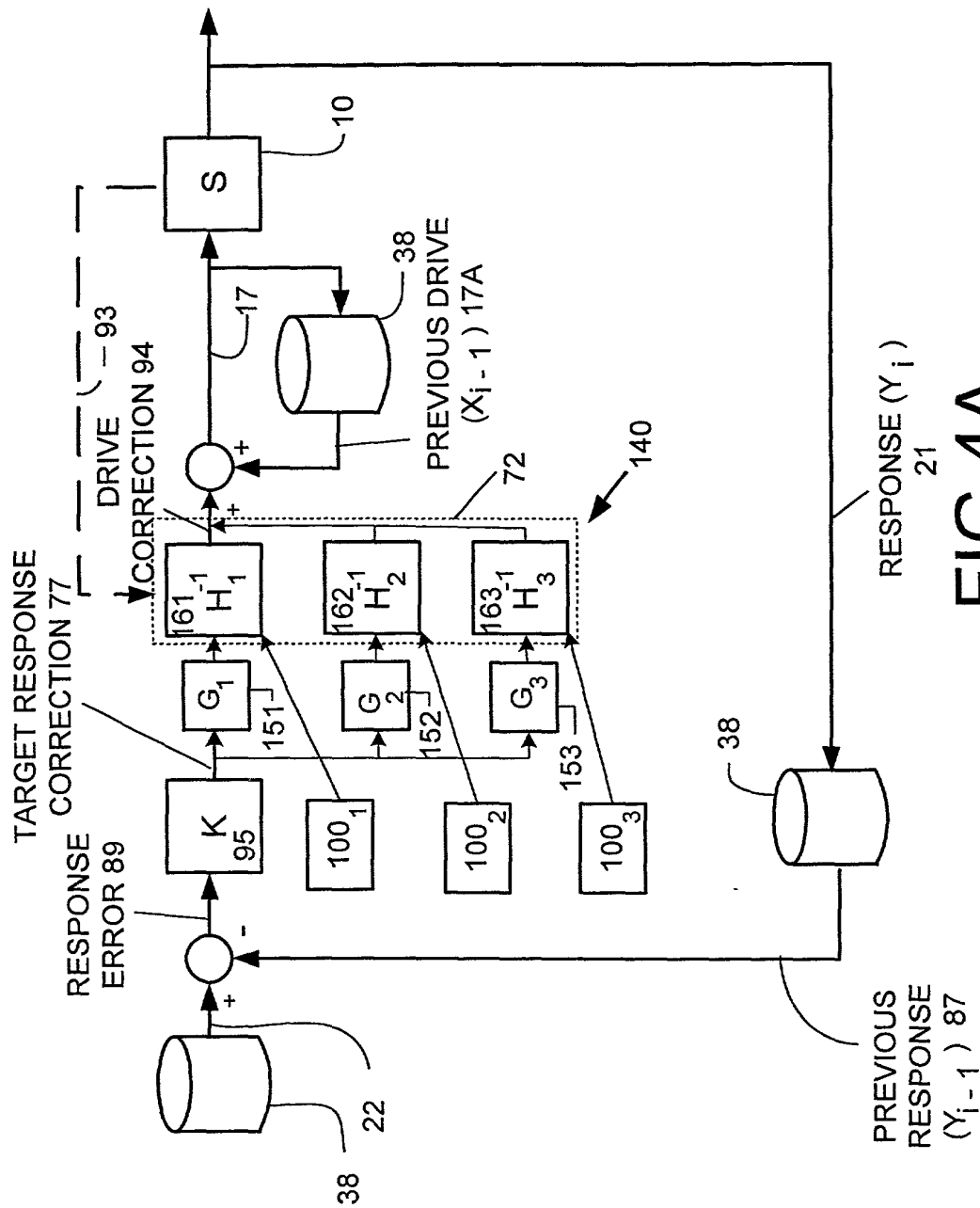


FIG. 4A

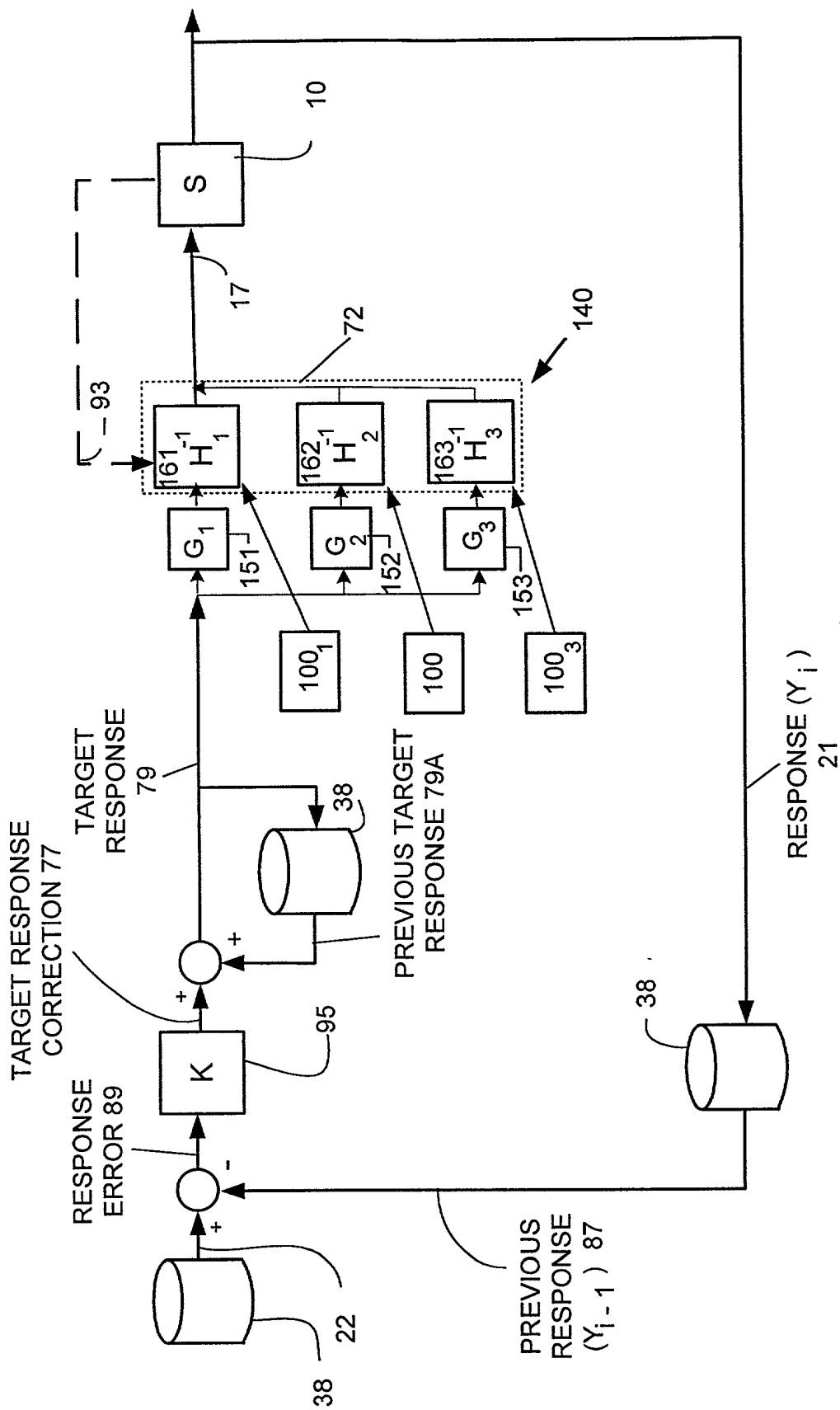


FIG. 4B

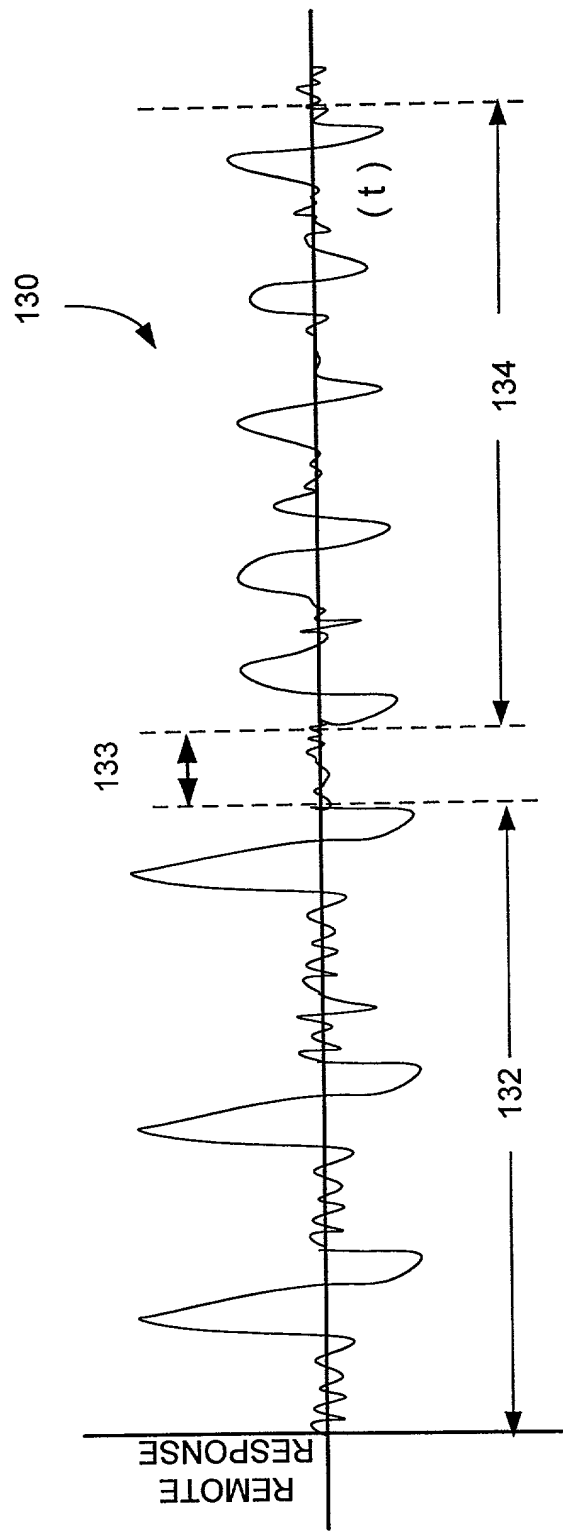


FIG. 5

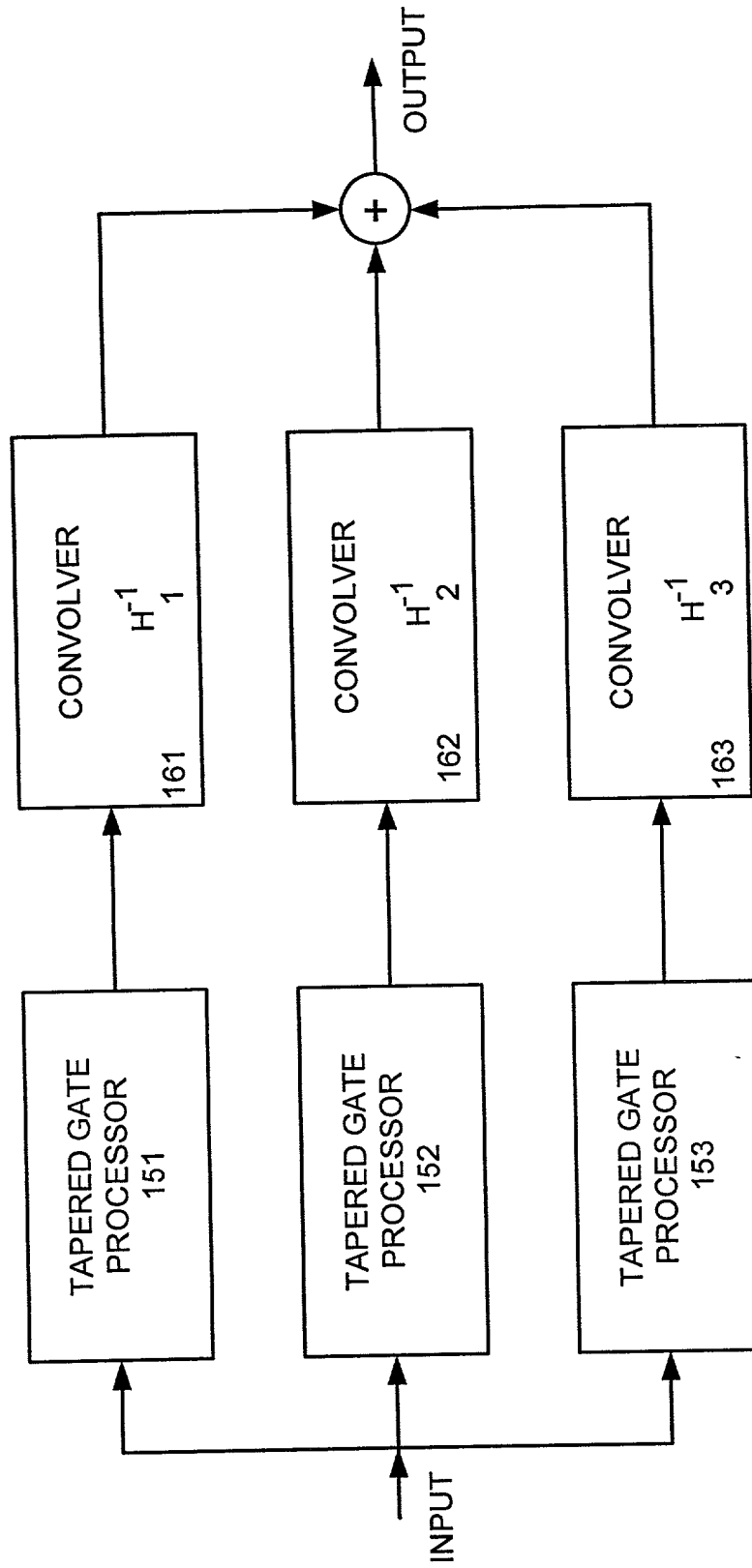


FIG. 6



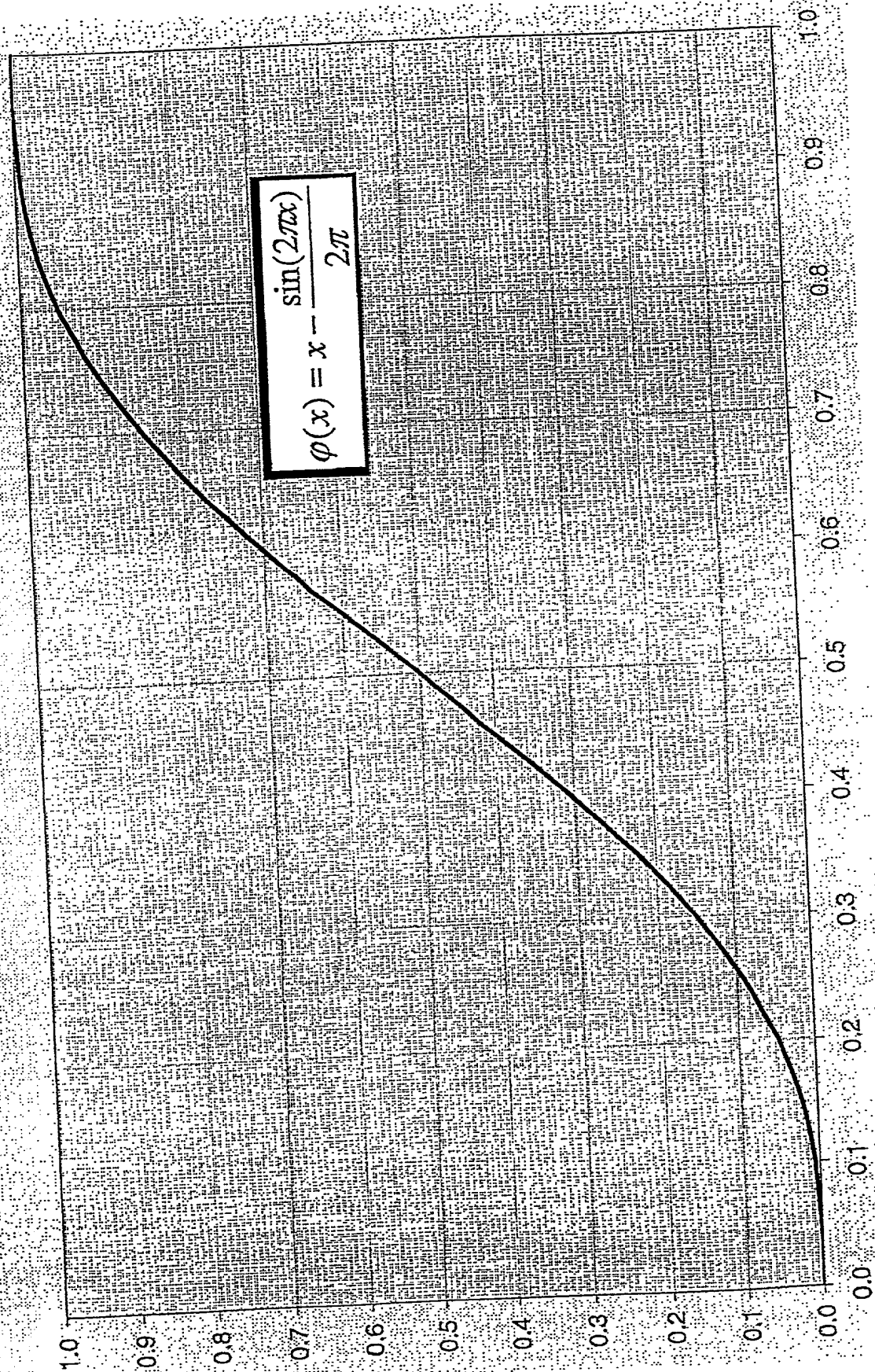


FIG. 7

0950626 093101

155

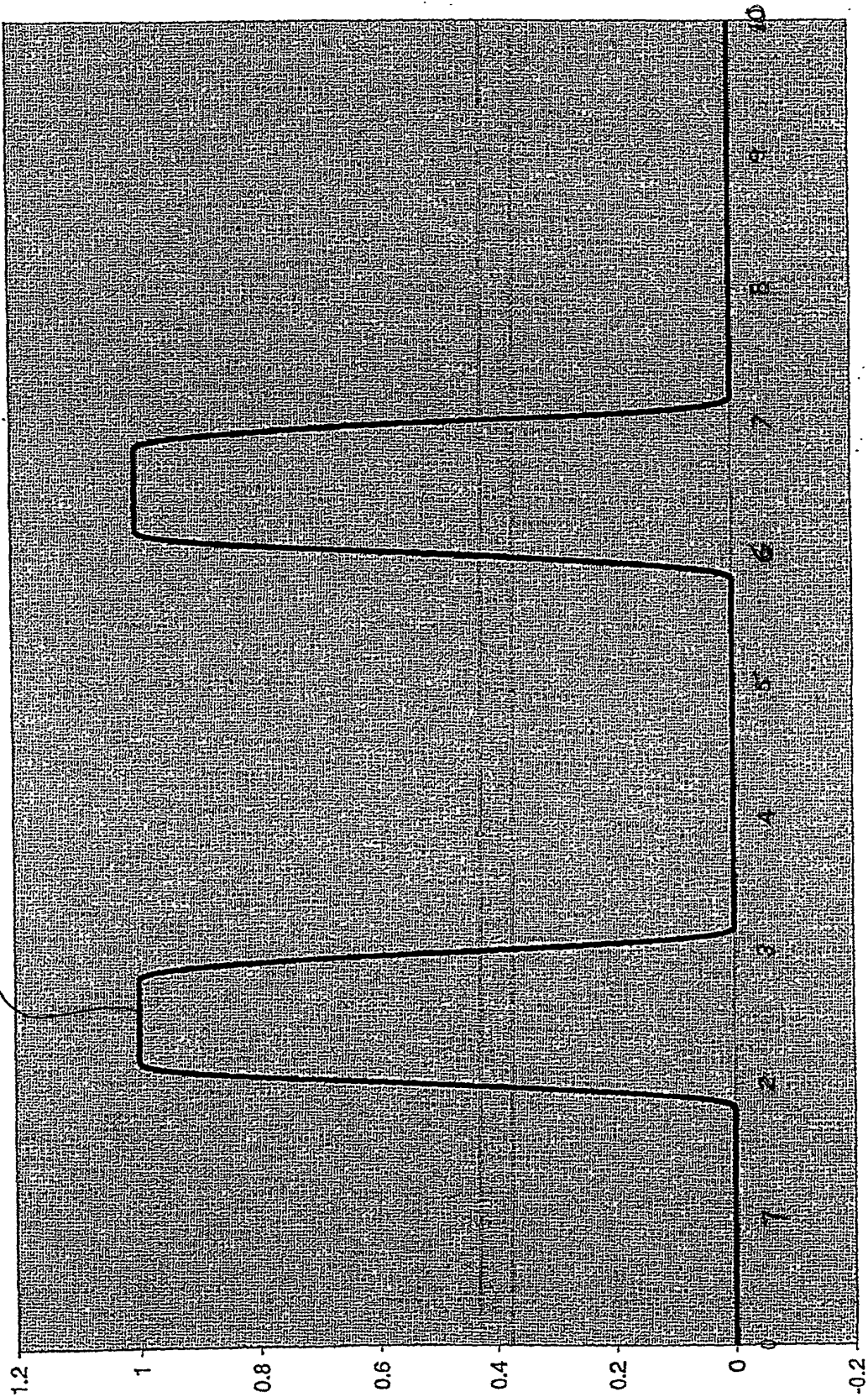


FIG. 8

FOI 260 92909660

157

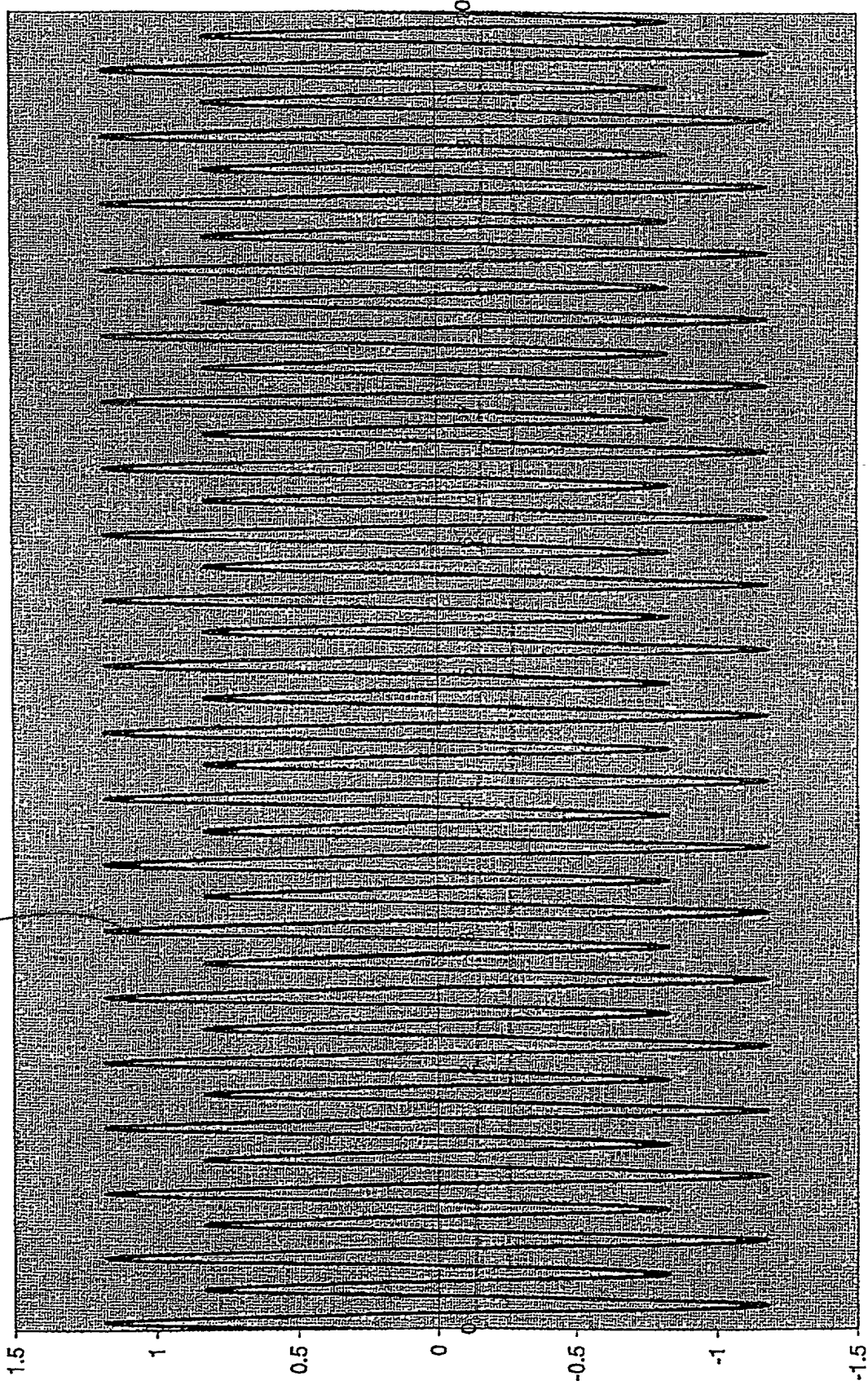


Fig. 9



92909660

- Gated Input Signal

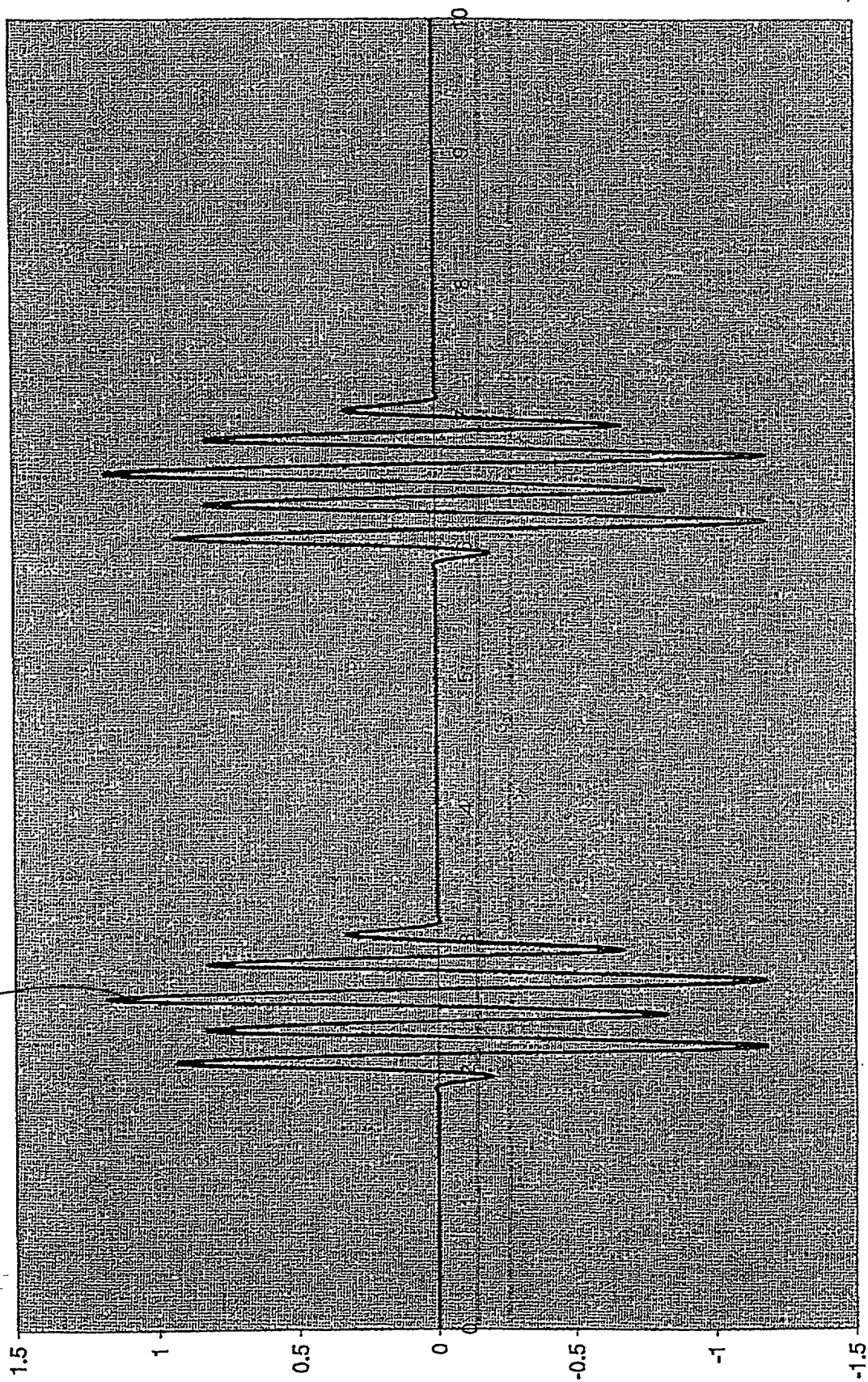


Fig. 10

FIG. 11

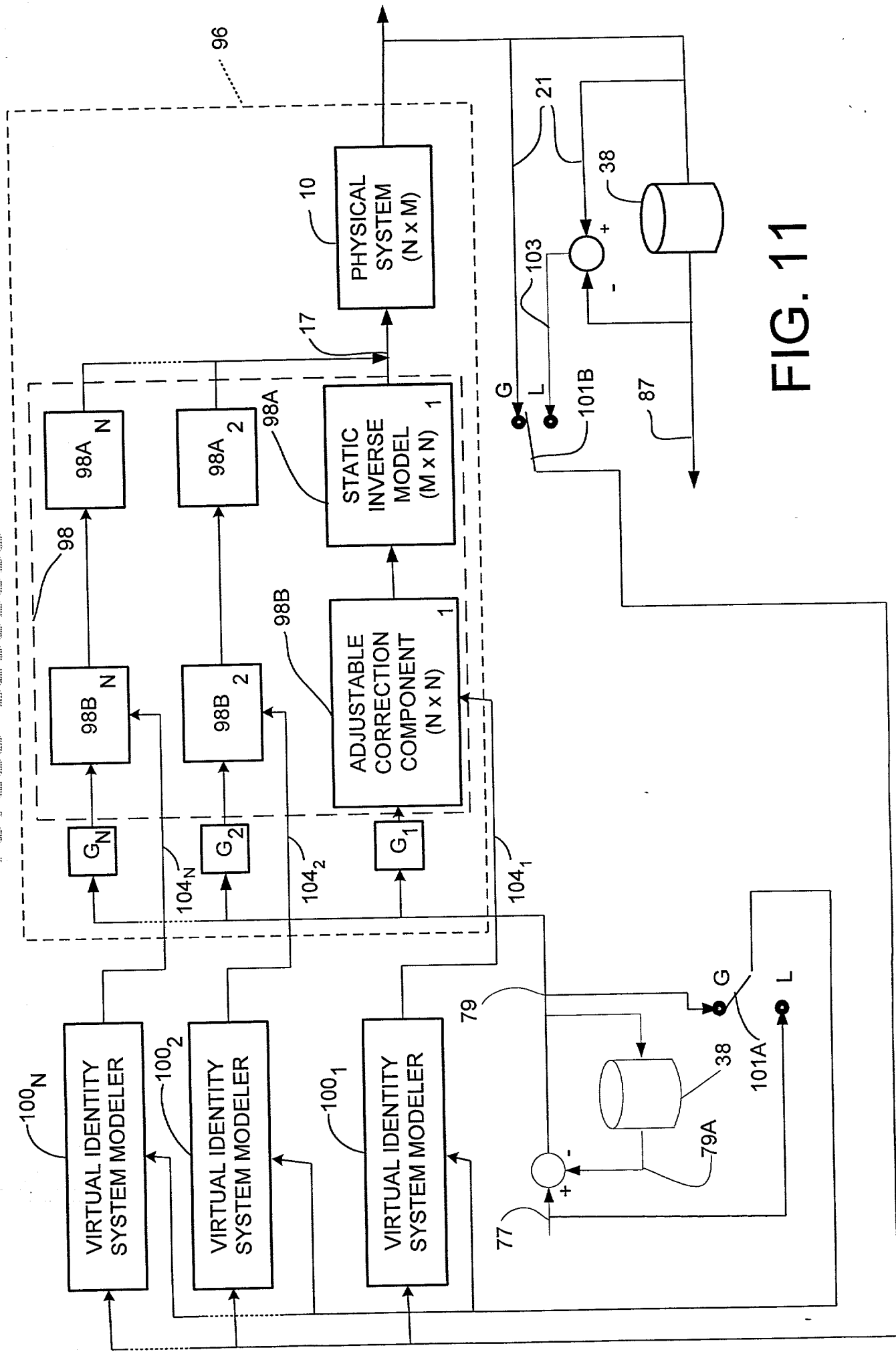


FIG. 11

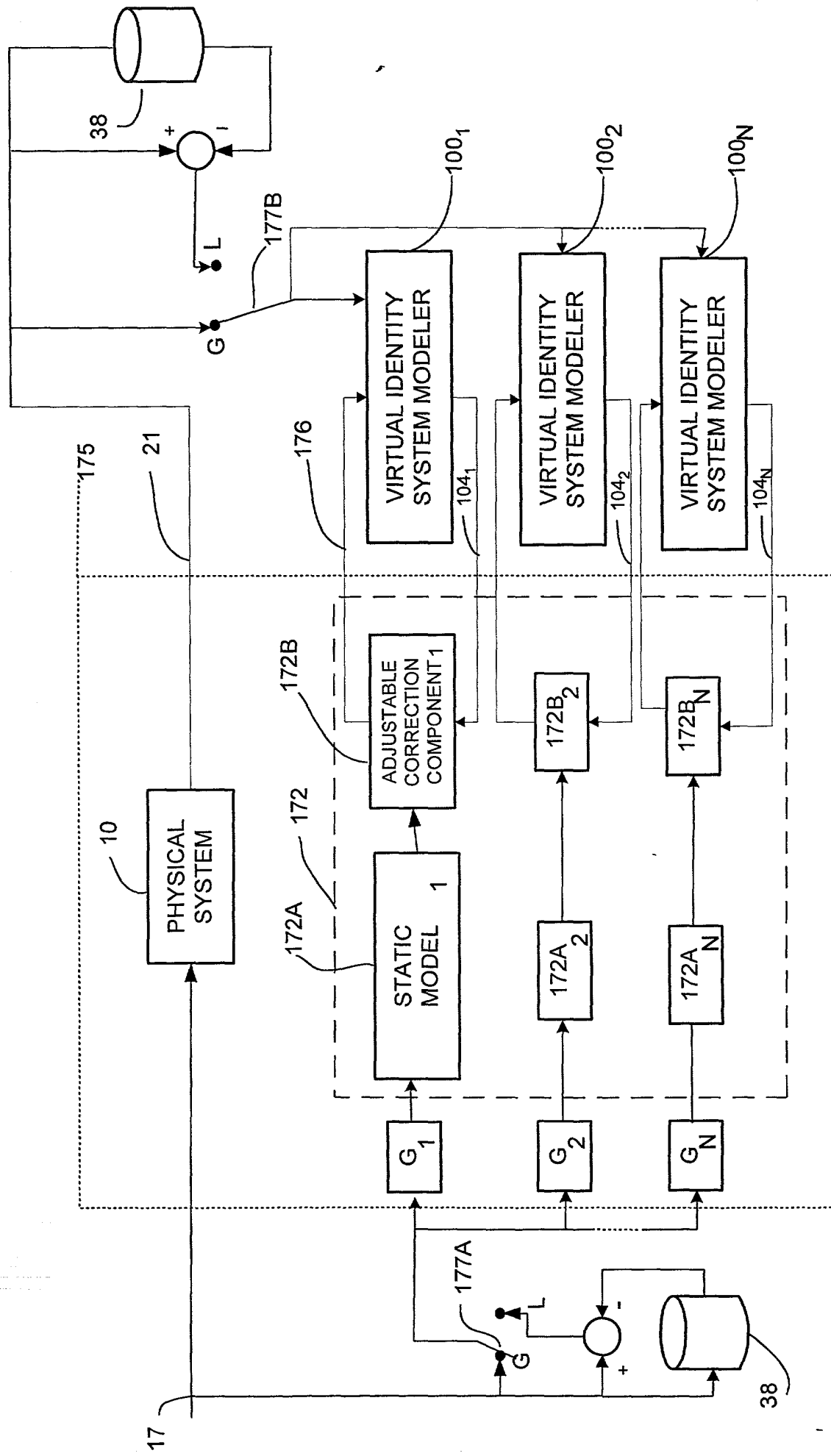


FIG. 12